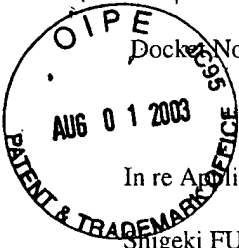


2815



Docket No.: 60188-084

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: Customer Number: 20277

Shigeki FURUYA, et al.

: Confirmation Number: 1398

Serial No.: 09/922,671

: Group Art Unit: 2815

Filed: August 07, 2001

: Examiner: Matthew E. WARREN

For: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

☐
☐
☐

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	27	20	7	\$18.00 =	\$126.00
Independent Claims	10	3	7	\$84.00 =	\$588.00
Multiple claims newly presented					\$280.00
Fee for extension of time					\$0.00
Total of Above Calculations					\$994.00

☒ Please charge my Deposit Account No. 500417 in the amount of \$994.00. An additional copy of this transmittal sheet is submitted herewith.

☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Date: August 1, 2003

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